

Please replace the paragraph on page 9, lines 3-11, with the following amended paragraph:

a³

In one embodiment, error detection device and buffer 514 buffers packets that are received out of order. When enough packets have been received to re-create the correct transmitted order, error detection device and buffer 514 outputs packets in the correct order on node 518. The re-creation of the correct transmitted order is advantageous in part because decryptor 520 receives encryption keys, via node 522, that are synchronized with encryption keys used at the transmitter. When packets are received in order at decryptor 520, synchronization is maintained. In contrast, when packets are not received in order at decryptor 520, synchronization can be lost. In either case, decrypted voice data are output via node 524, and decrypted non-voice data are output via node 526.

Please replace the paragraph on page 10, lines 1-14, with the following amended paragraph:

a⁴

FIG. 6 shows a mask store in accordance with a preferred embodiment of the present invention. Mask store 600 includes a list of ordering masks 610-1, 610-2, 610-3, 610-4, . . . 610-N. Also shown in FIG. 6 is ordering mask pointer 620. Ordering mask pointer 620 can point to a current ordering mask. When mask store 600 is utilized in a transmit processing block, such as mask store 424, the current ordering mask is the ordering mask corresponding to the most recently transmitted packet. When mask store 600 is utilized in a receive processing block, such as mask store 506, the current ordering mask is the ordering mask corresponding to the most recently received packet. As packets are transmitted and received, current ordering mask pointer 620 is advanced in the list of ordering masks within mask store 600. For example, mask A can be applied to a first packet, mask B can be applied to a second packet, and mask C can be applied to a third packet. After mask C has been applied, current ordering mask pointer 620 points to mask C as shown in FIG. 6.

Please replace the paragraph that runs from page 10, line 29 to page 11, line 5, with the following amended paragraph:

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FIG. 7 shows a transmit processing block in accordance with an alternate embodiment of the present invention. Transmit processing block 700 includes forward error device 706, encryptor/masking device 716, and controller 710. Transmit processing block 700 does not encrypt packets prior to forward error device 706. Forward error device 706 receives voice packets on node 702 and data packets on node 704, and it applies forward error codes thereto. Packets having error codes applied are sent to encryptor/masking device 716 on node 712. In one embodiment, encryptor/masking device 716 encrypts packets and masks them in a single operation to supply encrypted/masked packets on node 718. Encryption keys are received on node 714, and they are used to encrypt packets received on node 712. The encryption keys received on node 714 are also used to mask the packets. Because the order of encryption keys is known at both the transmitter and the receiver, encryption keys can be used as ordering masks.